

Substitute for form 1449A/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT <i>(use as many sheets as necessary)</i>				Complete If Known	
				Application Number	09/749,088
				Filing Date	December 26, 2000
				First Named Inventor	Mitsuhiro Adachi
				Art Unit	2116
				Examiner Name	James K. Trujillo
				Attorney Docket Number	42390P10138
Sheet	1	of	2		

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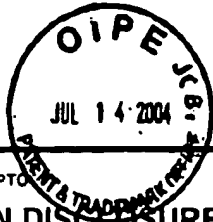
Examiner Signature	<i>Lorne H. Smith</i>	Date Considered	08/29/05
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*Examiner: Initial if reference considered, whether or not citation is in conformance with MPEP 608. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication.

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NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No.†	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T‡
AKT		JACOB MILLMAN, Ph.D., Charles Batchelor Professor, Emeritus, Columbia University, "Microelectronics, Digital and Analog Circuits and Systems," McGraw-Hill Book Co., 1979, pps. 673-676	
		RAYMOND M. WARNER, JR., Director of Engineering, Motorola Inc., Semiconductor Products Division, "Integrated Circuits, Design Principles and Fabrication," McGraw Hill Book Co., 1965, pps. 362-368	
		RAY P. PRASAD, SMT Program Manager, Intel Corporation, "Surface Mount Technology, Principles and Practice," Van Nostrand Reinhold, New York, 1989, pps. 178-181	
		ADEL S. SEDRA, University of Toronto, KENNETH C. SMITH, University of Toronto and Hong Kong, "Microelectronic Circuits," 4th Ed., Oxford Univ. Press, 1998, pps. 773-778	
		"Intel486™ Microprocessor and Related Products," Microprocessors, PCIsets, Peripheral Components, Intel Corporation, 1995, pps. 2-304, 2-313 to 2-318	
		VLADIMIR ALEKSIEV, "Thermal Design for High Performance Notebooks," Intel Corporation, Order No. 241812-001, October 1993, pps. 2-493 to 2-508	
		CHENG XIE, MCG Technical Marketing, "Managing Power with the SL Enhanced Intel486™ Microprocessor," Intel Corporation, Order No. 241811-001, October 1993, pps. 2-485 to 2-490	
AKT		PHILIP BRACE, "Clock Throttling the SL Enhanced Intel486™ CPU in a Networked Environment," Intel Corporation, Order No. 421988-001, May 1994, pps. 2-513 to 2-541	

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Examiner Signature	<i>James K. Trujillo</i>	Date Considered	9/21/04
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